

(Rev. 5/22/02) Information Disclosure Statement List By Applicant(s) Under 37 CFR Section 1.98(a)(1) (Use several sheets if necessary)		Attorney Docket Number: AVX-247-DIV.	Serial Number: 10/829,639
		Applicant: ANDREW P. RITTER et al.	
		Filing Date: April 22, 2004	Group Art Unit: 2812
		Confirmation No: 7029	

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U.S. PATENT DOCUMENTS

EXAMINER INITIALS	PATENTEE NAME	PATENT NUMBER							ISSUE DATE	COPY NOTE
	Garibotti	3	2	5	8	8	9	8	07/1966	2,5
	Maher	3	9	8	8	4	9	8	10/1976	2,5
	Dorrian	4	2	4	1	3	7	8	12/1980	2,5
	Maher	4	2	6	6	2	6	5	05/1981	2,5
	Ling et al.	4	8	1	1	1	6	4	03/1989	2,5
	Lutz et al.	4	9	1	9	0	7	6	04/1990	2,5
	Sano et al.	5	0	2	1	9	2	1	06/1991	2,5
	Braden	5	2	2	6	3	8	2	07/1993	2,5
	Gracia et al.	5	7	5	3	2	9	9	05/1998	2,5
	Stone	5	7	7	0	4	7	6	06/1998	2,5
	Braden et al.	5	8	6	3	3	3	1	01/1999	2,5
	Zablotny et al.	5	8	8	0	0	1	1	03/1999	2,5
	DuPré et al.	5	8	8	0	9	2	5	03/1999	2,5
	Braden	5	9	4	4	8	9	7	08/1999	2,5
	Miki	6	1	4	1	8	4	6	11/2000	2,5
	Naito et al.	6	1	8	8	5	6	5	02/2001	2,5
	Kuroda et al.	6	1	9	1	9	3	2	02/2001	2,5
	Clinton et al.	6	2	1	4	6	8	5	04/2001	2,5
	McLoughlin	6	2	3	2	1	4	4	05/2001	2,5
	Ahiko et al.	6	3	8	0	6	1	9	04/2002	2,5
	Kuroda et al.	6	5	9	4	1	3	6	07/2003	2,5

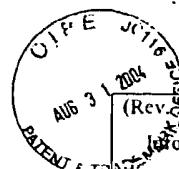
FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	COUNTRY	DOCUMENT NUMBER	PUBLICATION DATE	TRANSLATION			COPY NOTE
				YES	NO	N/A	
BF	Japan	6 1 6 8 8 4 5	06/1994		X		2
BT	Japan	2 1 6 4 2 5 7	06/2002		X		2

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EXAMINER INITIALS	OTHER DOCUMENTS Specify author (if any), Title, Pertinent Pages, Date & Place of Publication	COPY NOTE
BT	Translated Abstract of Japanese Patent No. 6168845 cited on previous page	2
/	Translated Abstract of Japanese Patent No. 2164257 cited on previous page	2
/	Hung Van Trinh, "An Electrodeposition Method for Terminals of Multilayer Ceramic Capacitors," March 23, 2002 (a thesis submitted in partial satisfaction of the requirements for the degree Master of Science in Materials Science and Engineering, University of California-San Diego)	2
/	Hung Van Trinh and Jan B. Talbot, "An Electrodeposition Method for Terminals of Multilayer Ceramic Capacitors" CARTS 2003: 23rd Capacitor and Resistor Technology Symposium March 31-April 3, 2003	2
BT	Hung Van Trinh and Jan B. Talbot, "Electrodeposition Method for Terminals of Multilayer Ceramic Capacitors," June 2003, Vol. 86, No. 6, Journal of the American Ceramic Society	2
EXAMINER <i>B. Talbot</i>	DATE CONSIDERED <i>11/4/04</i>	
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(Rev. 5/92)

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By Applicant(s)
Under 37 CFR Section 1.98(a) (1)
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ANDREW P. RITTER et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIALS	PATENTEE NAME	PATENT NUMBER						ISSUE DATE	COPY NOTE
BS	Monroe et al.	3	6	9	5	4	5	09/1887	S
	Aheam, Jr., et al.	3	4	4	8	3	5	06/1969	S
	Weller	3	8	9	8	5	4	08/1975	S
	McElroy et al.	3	9	9	2	7	6	11/1976	S
	Maher et al.	4	8	1	1	1	6	03/1989	S
	Senda et al.	5	5	7	6	0	5	11/1996	S
	Ahn	6	1	5	9	7	6	12/2000	S
	Dupré et al.	6	2	4	3	2	5	06/2001	S
BS	Tuzuki et al.	6	3	1	0	7	5	10/2001	S

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	COUNTRY	DOCUMENT NUMBER	PUBLICATION DATE	TRANSLATION			COPY NOTE
				YES	NO	N/A	
BT	GB	1 5 4 0 4 0 3	02/1979			X	
BT	JP	01 3 1 3 8 0 4	12/1989		X		
BT	EP	0 3 5 1 3 4 3	01/1990			X	

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BS	Translated Abstract of Japanese Patent No. 1-313804 cited above	
BS	European Search Report for Application No. GB0308656.8 dated May 6, 2004	
BS	European Search Report for Application No. GB0405993.7 dated July 26, 2004	
EXAMINER	<i>BS, Ta/1607</i>	DATE CONSIDERED <i>11/16/04</i>
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